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CORRECTION

Correction to: Simultaneous multiprocessing in a software-defined heterogeneous FPGA

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The presentation of Table 2 was incorrect in the original article. The correct Table 2 is given below. The original article has been corrected.

The original article can be found online at https://doi.org/10.1007/s11227-018-2367-9.

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Table 2 Energy change when the second CPU core is utilised

	AES (%)	HotSpot (%)	GEMM (%)	Nbody (%)
Dynamic with interrupt	− 7.73	+ 22.93	− 1.52	− 1.64
Dynamic without interrupt	− 1.53	+ 2.14	+ 13.64	+ 11.67
LogFit with interrupt	− 19.51	+ 16.54	+ 2.87	+ 6.89
LogFit without interrupt	+ 16.00	+ 10.16	+ 1.12	+ 7.59

Negative values indicate improvement in energy consumption. The lower the better